

SSSC FACTS Device in Reactive Power Flow Solution

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Abstract - This paper describe the reactive power flow solution based BBO to amend the performance of the power system. Biogeography-based optimization is incorporating flexible AC transmission systems (FACTS). Static Synchronous Series Compensator (SSSC) is type of FACTS device used in this paper. In this BBO Store best parent solution and apply mutation and migration process on remaining parents to produce best fitted child sets. This paper define the problem of optimal power flow solution is very severe in modern interconnected transmission system the control of reactive and real power has to be fast to insure that the system remains stable under all condition of operation. The use of thyristor based controllers enable a transmission system to be flexible using SSSC FACTS is a series connected FACTS controller. The proposed BBO method gives better solution quality compared to particle swarm optimization with static synchronous series compensator facts device. The simulation results show that the proposed BBO algorithm is effective, fast and accurate in finding the optimal parameter settings for FACTS devices to solve OPF problems. BBO algorithm is tested on IEEE 14-bus with SSSC FACTS device gives better solution to enhance the system performance.

Keywords- Power system operation, FACTS, Biogeography based optimization, optimal power flow, SSSC Device.

I. INTRODUCTION

The AC transmission system has different limits classified as stationary limits and dynamic limits. These inherent power system limits restrict the power deal, which guide to the beneath utilization of the active transmission resources. Conventionally, fixed or mechanically switched Series and series capacitors, reactors and synchronous generators were being used to solve much of the difficulty. Though, there are limitations as to the use of this conventional equipment. Wanted performance was not being able to attain efficiently. Wear and tear in the mechanical apparatus and sluggish response were the heart of the trouble. There was better requiring for the substitute technology made of solid state devices with quick response characteristics.

They require was extra fuelled by universal reformation of electric utilities, rising environmental and efficiency regulations and difficulty in realization authorize and accurate of technique for the construction of overhead transmission lines. This, jointly with the development of Thyristor switch (semiconductor gadget), opened the gate for the growth of power electronics devices known as Flexible AC Transmission Systems (FACTS) controllers. The path from historical Thyristor based FACTS controllers to present state of the skill voltage source converters based FACTS.

II. STATIC SYNCHRONOUS SERIES COMPENSATOR (SSSC)

Static Synchronous Series Compensator (SSSC) is single of the main series FACTS devices. SSSC is a solid state voltage source inverter I insert an approximately sinusoidal voltage, of variable magnitude in series with transmission line. The injected voltage is approximately in quadrature with the line current. A little part of the injected voltage, which is in phase with the line current, provides the losses in the inverter. Mainly of the injected voltage, which is in quadrature with the line current, emulates an inductive or a capacitive reactance in series withthe transmission line. This emulated variable reactance, inserted by the injected voltage source, influences the electric power flow through the transmission line.

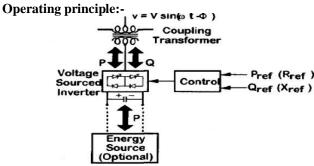


Fig.1 Block diagram of Stat Synchronous Series Compensator

III. RESULT AND SIMULATION

Nominal Power (Pn)– 3730 VA, Voltage (line -line) (Vn)– 338 Vrms, Frequency (Fn) – 50 Hz , Stator Resistance (Rs) – 1.115 ohm and Inductance (Lls)– 0.005974 H, Rotor Resistance (Rr`)-1.083 ohm and Inductance (Llr`)- 0.005974 H, Mutual Inductance (Lm)– 0.2037 H, Inertia (J) 0.02 Kgm2 , Friction Factor (F) – 0.005752 N.M.S. , Pole Pair (p) – 2,Lc= 5mH, Rc = 0.1 Ω , hb=0.5A, C1=5000F, Rl = 1000 Ω , P = 0.43, I = 0.15, D = 0

1. Performance of D-SSSC for Harmonics Elimination of linear Load

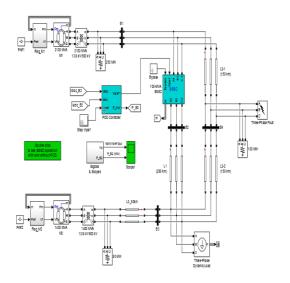


Fig.2 Simulated System of D-SSSC for Harmonics Elimination of linear Load.

2. Simulated design of Control Strategies for System.

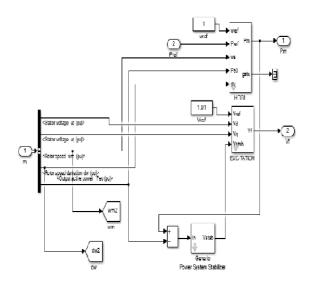


Fig. 3 Simulated design of Control Strategies for System.

3. Performance of SSSC for Harmonics Elimination of Nonlinear Load

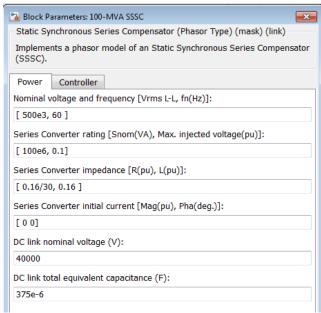


Fig. 4 Simulated System of SSSC for Harmonics Elimination of Nonlinear Load

4. Performance of D-SSSC for Harmonics Elimination of Induction Motor Load:-

Simulation Results:-

For Linear Load:-

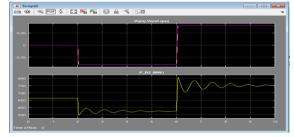


Fig.5 Source voltage (Vs)

Fig. 5 shows the source voltage (I_s) of the linear load of SSSC system. And fig. 7.6 shows the load current of the linear load (I_L) of the SSSC system.

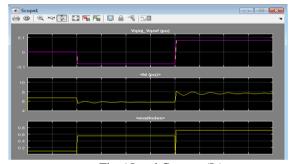


Fig.6 Load Current (I_L)



Fig.7 Compensating Current (I_C)..

Fig. 7 shows the Compensating Current (I_c) of the linear load of D-SSSC system. And fig. 3.7 shows the Gating pulse (I_g) of the linear load of the D-SSSC system

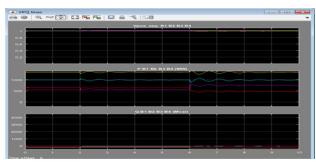


Fig.8 Gate Pulse (Ig).

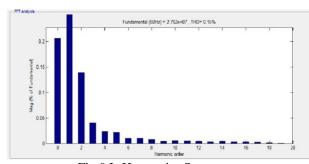


Fig.9 I_S Harmonics Spectrum.

Fig. 8 shows the FFT analysis of source current (I_s) of the linear load of D-SSSC system. And fig. 9 shows the FFT analysis of load current (I_L) of the linear load of the D-SSSC system.

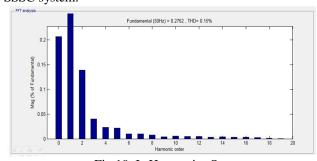


Fig.10 I_L Harmonics Spectrum

5. For Non Linear Load:-

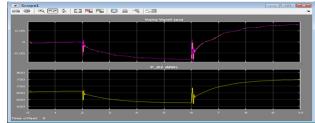


Fig.11 Source voltage (v_S).

Fig. 11 shows the source voltage (I_s) of the nonlinear load of SSSC system. And fig.12 shows the load current of the nonlinear load (I_L) of the SSSC system.

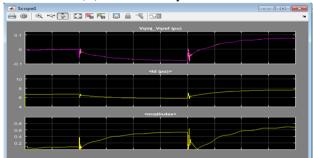


Fig. 12 Load Current (I_L).

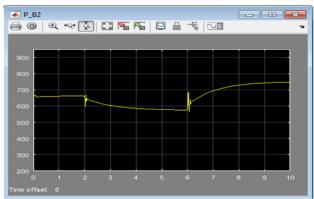


Fig.3.12 Compensating Current (I_C).

Fig.12 shows the Compensating Current (I_c) of the non linear load of SSSC system. And fig. 13shows the Gating pulse (I_g) of the non linear load of the SSSC system.

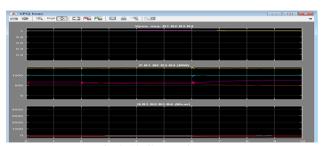


Fig.3.13 Gate Pulse (I_g)

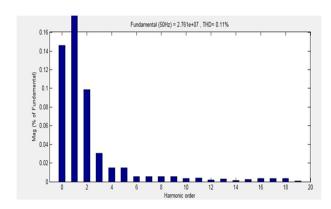


Fig. 14 I_S Harmonics Spectrum.

Fig.14 shows the FFT analysis of source current (I_s) of the non linear load of D-SSSC system. And fig. 3.15 shows the FFT analysis of load current (I_L) of the non linear load of the D-SSSC system.

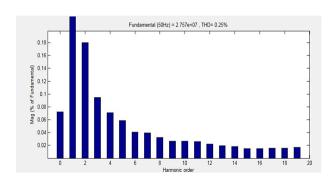


Fig.15 IL Harmonics Spectrum.

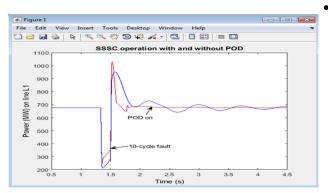


Fig.16 I_S Harmonics Spectrum.

Fig. 16 shows the FFT analysis of source current (I_s) of the nonlinear load of SSSC system. And fig. 3.17 shows the FFT analysis of load current (I_L) of the nonlinear load of the SSSC system.

Table 1 Comparative analysis of OSCILLATION through Compensated and Uncompensated system

Types of Load	OSCILLATION % Without Compensation		OSCILLATION % With Compensation	
	i_{S}	iL	i _S	iţ
Linear Load	3.21	3.21	0.15	0.15
Non Linear Load	25.34	25.34	0.11	0.25
Induction Motor Load	26.86	26.86	0.21	2.58

IV. CONCLUSION

- A model of SSSC has been developed in MATLAB environment using Power System Block-set. The performance of the developed model is tested under a wide variety of loading conditions.
- It is found that SSSC is capable of minimizing the harmonics and reactive power compensation.
- Indirect current control technique has been applied over the sensed and reference supply currents for SSSC and it has been found to be a simple technique. Only one PI controller is required to regulate terminal voltage and thus reduces computation effort.
- The control algorithm of the SSSC is flexible and has been tested for power quality improvement for linear as well as nonlinear and Induction motor loads.
- D-SSSC is able to reduce harmonics in voltage at PCC and supply currents to less than 5% IEEE 519 standards. SSSC reduces harmonics in load current to a large extent and provides quality power.

V. FUTURE IMPLICATIONS

- The simulation has been carried out in MATLAB/SIMULINK environment and power factor is unity for supply voltage and current.
- To analyze the effect of non-linear loads, linear loads and Motor loads on Distribution system when feeding a generation with wind and solar.
- Modelling of reactive power theory and compare results with ICCT.
- To Study and Simulation of Fuzzy and ANN based controller on behalf of PI controller.
 In a future work, the obtained simulation results will be compared with experimental results, to be measured in a developed D-SSSC prototype.

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